Applicant:

IN THE SITED STATES PATENT AND TR



Sarral No:

Jie Liang 09/517,234

Filed:

03/02/2000

The item(s) checked below are appropriate:

Title:

Art Unit:

2624

Examiner:

A. Do

Docket No.: Conf. No.:

TI-29011 4622

Assistant Commissioner

Washington, DC 20231

Image Coding Using Embedded Zerotree Patterns and Bitplanes RECEI

JUL 1 5 2003

Technology Center 2600

NOTICE OF APPEAL

MAILING CERTIFICATE

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231

Dear Sir:

for Patents

Applicant hereby appeals to the Board of Appeals from the decision of the Primary Examiner dated 04/07/2003 all rejected claims.

An extension of time to respond to the final rejection 1. was granted on is requested for month(s). A timely response to the final rejection has been filed. 2. Fee \$320.00: 3. Χ Not required (Fee paid in prior appeal). The Commissioner is hereby authorized to charge any fees Χ which may be required, or credit any overpayment to the Deposit Account of Texas Instruments Incorporated, Account No. 20-0668. An original and one copy of this sheet 07/14/2003 AWONDAF1 00000072 200668 09517234 are enclosed. 01 FC:1401 320.00 DA

Signature [Rule 191(b)]

Carlton H. Hoel

Registration No. 29,934

Texas Instruments Incorporated PO Box 655474, M/S 3999

Dallas, TX 75265 (972) 917-4365

Post Office Address

(to which correspondence is to be sent)